



316	Subclass
257	Class

ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

M.H SCANNED	O.I.P.E. PDS	PATENT DATE O.A. AG.
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03-1564
JC
TG 861

CLASSIFICATION NO. 09/620649	CONT/PRIOR D	CLASS 257	SUBCLASS 316	ART UNIT 2822	EXAMINER Wilczewski
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APPLICANTS
Cetin KayaTITLE
Integrated circuit having independently formed array and peripheral isolation dielectricsPTO-2040
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
257	316	438	257		
INTERNATIONAL CLASSIFICATION					
H01L 29/788					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	5	10	4d1	1	1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	Mary Wilczewski Primary Examiner <i>M.Wilcz</i> 2/2004 (Primary Examiner) (Date)			02/11/04	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE FEE	
				Amount Due	Date Paid
				\$1330	
ISSUE BATCH NUMBER					
(Legal Instruments Examiner) (Date)					

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